

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
25 January 2001 (25.01.2001)

PCT

(10) International Publication Number  
WO 01/06637 A1

(51) International Patent Classification<sup>7</sup>: H03B 5/12

(21) International Application Number: PCT/GB00/02759

(22) International Filing Date: 18 July 2000 (18.07.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
9916901.3 19 July 1999 (19.07.1999) GB

(71) Applicant (for all designated States except US): CAMBRIDGE SILICON RADIO LTD. [GB/GB]; Unit 300, Science Park, Milton Road, Cambridge CB4 0XL (GB).

(72) Inventors; and

(75) Inventors/Applicants (for US only): COLLIER, James, Digby, Yarlet [GB/GB]; Church Farm, Chettisham, Ely, Cambridgeshire CB6 1SB (GB). SABBERTON, Ian, Michael [GB/GB]; Valentine Cottage, Weston Green, Cambridgeshire CB1 5NS (GB).

(74) Agents: SLINGSBY, Philip, Roy et al.; Page White & Farrer, 54 Doughty Street, London WC1N 2LS (GB).

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

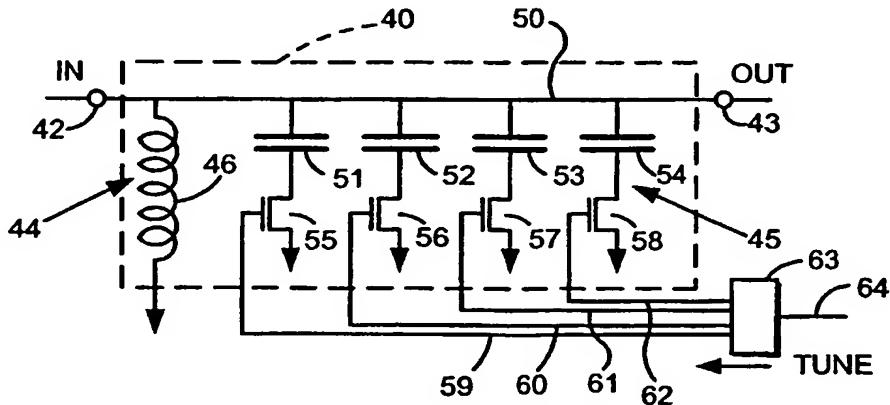
(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

— With international search report.  
— Before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: ADJUSTABLE FILTER



(57) Abstract: A radio transmitter and/or receiver comprising: an oscillator tuning circuit comprising an adjustable capacitor whose capacitance is adjustable by means of a first tuning signal; a filter tuning circuit comprising an adjustable capacitor whose capacitance is adjustable by means of a second tuning signal; an oscillator whose operational frequency is dependant on the reactance of the oscillator tuning circuit; a filter for filtering signals in the course of transmission and/or reception, and whose response is dependant on the reactance of the filter tuning circuit; and a tuning unit for generating the first and second tuning signals; wherein at least a part of the filter tuning circuit is a replica of at least a part of the oscillator tuning circuit and the tuning circuit is capable of generating one of the first and second tuning signals in dependence on the other of the tuning signals.

WO 01/06637 A1

## ADJUSTABLE FILTER

This invention relates to the adjustment of filters, especially in ways that can address manufacturing variations. The filters may be usable in transceivers for transmitting and/or receiving radio signals.

Figure 1 shows schematically the structure of one example of a radio transceiver. The transceiver has an antenna 1 and a signal processing unit 2 for baseband processing of received signals and signals that are to be transmitted. Between the antenna and the signal processing unit are a receive chain 3 and a transmit chain 4, which are connected to the antenna 1 by a duplexer 5. The receive chain 3 converts received radio frequency (RF) signals down to baseband for further processing by the signal processing unit 2. The transmit chain 4 converts signals from baseband to RF for transmission from the antenna 1. The receive chain comprises an input amplifier 7 which amplifies the received signal, a mixer 8 which mixes the amplified signals with a signal from oscillator 9 to convert to intermediate frequency (IF), and a mixer 10 which mixes the IF signal with a signal from oscillator 11 to convert to baseband. Between each of the units 7, 8, 10 and 2 is a bandpass filter 12, 13, 14 to remove off-band interference and image frequencies. The transmit chain comprises a mixer 17 which mixes the baseband signals with a signal from oscillator 18 to upconvert to RF and an output amplifier 19 which amplifies the RF signals for transmission. Filters 20, 21, 22 can also be included in the transmit chain.

In many applications it would be desirable, in order to reduce size and cost, to implement the entire transceiver, or at least the receive and transmit chains on a single integrated circuit (IC). One particular difficulty in designing such an IC is the implementation of the filters and the oscillators, and especially of circuitry intended for setting the operational frequencies of those components. In many implementations of transceiver circuits the components of the filters (especially the RF filters 12, 20, 21) are implemented by passive components rather than as

active filters. Often these are provided as discrete off-chip components such as capacitors, inductors and ceramic or SAW (surface acoustic wave) filters even if the remainder of the transmit and receive chains is implemented on a single integrated circuit.

The approach of implementing filters by means of discrete components generally provides filters that have superior response, noise or linearity characteristics. However, there is a need to adjust such filters after manufacturing in order to tune them to the desired frequency response. For this reason adjustable discrete components such as trimmer capacitors are generally also provided to allow the filters to be tuned to the desired frequency response. This approach has a number of disadvantages. First, during manufacturing the fitting of the discrete components requires additional processing steps. Second, the discrete components demand additional space in the radio device, increasing its overall size. Third, additional time and additional processing stations are needed to adjust the filters mechanically to the desired response during the manufacturing process.

In several situations there is correspondence between adjustment desired to be made to an oscillator of the transceiver (e.g. oscillator 18) and a filter of the transceiver (e.g. filter 20). This can arise because (i) when a channel is selected for transmission and reception the filter and the oscillator may have to be adjusted correspondingly so that the pass band of the filter and the oscillation frequency of the oscillator are the same or related by simple algebraic expressions, and (ii) because when the filter and the oscillator are on the same integrated circuit they are likely to be subject to similar systematic errors due to environmental factors.

It would be desirable to employ a filter that has the generally superior characteristics of a filter formed from discrete components but that can be implemented on chip, and that could be adjusted efficiently.

According to the present invention there is provided a radio transmitter and/or receiver comprising: an oscillator tuning circuit comprising an adjustable capacitor whose capacitance is adjustable by means of a first tuning signal; a filter tuning circuit comprising an adjustable capacitor whose capacitance is adjustable by means of a second tuning signal; an oscillator whose operational frequency is dependant on the reactance of the oscillator tuning circuit; a filter for filtering signals in the course of transmission and/or reception, and whose response is dependant on the reactance of the filter tuning circuit; and a tuning unit for generating the first and second tuning signals; wherein at least a part of the filter tuning circuit is a replica of at least a part of the oscillator tuning circuit and the tuning circuit is capable of generating one of the first and second tuning signals in dependence on the other of the tuning signals.

The said part of the oscillator tuning circuit suitably comprises a plurality of selectively engageable reactive elements on which the reactance of the filter tuning circuit is dependant. The reactive elements may be capacitors or inductors. The reactive elements are preferably discrete components. The reactive elements are preferably formed on a single integrated circuit.

The said part of the filter tuning circuit suitably comprises a plurality of selectively engageable reactive elements on which the reactance of the filter tuning circuit is dependant. The reactive elements may be capacitors or inductors. The reactive elements are preferably discrete components. The reactive elements are preferably formed on a single integrated circuit. The said part of the filter tuning circuit preferably comprises a plurality of reactive elements each corresponding to a reactive element of the oscillator tuning circuit.

Most preferably the reactive elements of the said part of the filter tuning circuit are nominally of a common scale with respect to the corresponding elements of the said part of the oscillator tuning circuit. The reactive elements of the said part of the filter tuning circuit may be nominally identical to the corresponding elements of the said part of the oscillator tuning circuit. The said reactive elements of the said

part of the filter tuning circuit are nominally scaled as an integer multiple of the corresponding elements of the said part of the oscillator tuning circuit. The said reactive elements of the said part of the oscillator tuning circuit may be nominally scaled as an integer multiple of the corresponding components of the said part of the filter tuning circuit. The scaling may be in size, nominal value or actual value.

Each of the said reactive elements of the oscillator tuning circuit may be selectively engageable by a respective oscillator tuning switch means (e.g. a transistor or other preferably electrically actuatable switch) connected in series with it. Preferably each of the said oscillator tuning switch means is responsive to a digital component of the second tuning signal. Preferably each of the said reactive elements of the filter tuning circuit is selectively engageable by a respective filter tuning switch means connected in series with it. Each of the said filter tuning switch means is suitably responsive to a digital component of the first tuning signal. The first and second tuning signals may be provided on a plurality of individual digital lines. The first and second tuning signals may each comprise a plurality of digital signal components.

Preferably the operational frequency of the oscillator is an integer multiple or fraction of the frequency that lies in or near the pass band of the filter.

Most preferably the filter tuning circuit and the oscillator tuning circuit are formed on a single integrated circuit.

The filter is suitably part of a radio receiver unit of the transceiver, which is preferably a zero or near zero intermediate frequency receiver unit. The filter may be a passive filter. The oscillator may be a local oscillator for the transmitter and/or receiver.

The present invention will now be described by way of example with reference to the accompanying drawings, in which:

figure 2 shows a schematic drawing of a radio transceiver;

figure 3 shows an adjustable filter;  
figure 4 shows an adjustable oscillator;  
figure 5 shows an oscillator control circuit; and  
figure 6 shows an alternative adjustable filter.

Figure 2 shows part of a transceiver circuit having an antenna 31 and a signal processing unit 32 for baseband processing of received signals and signals that are to be transmitted. Between the antenna and the signal processing unit are a receive chain 33 and a transmit chain 34, which are connected to the antenna 1 by a duplexer 35. The antenna, signal processing unit and duplexer 35 are similar to those of figure 1. As in the system of figure 1 the receive chain 33 converts received radio frequency (RF) signals down to baseband for further processing by the signal processing unit 32, and the transmit chain 34 converts signals from baseband to RF for transmission from the antenna 31.

The receive chain comprises an input amplifier 37 which amplifies the received signal and a mixer 38 which mixes the amplified signals with a signal from oscillator 39. A bandpass filter 40 is located between the amplifier 37 and the mixer 38. The bandpass filter is intended only to pass signals at or very near the frequency of the RF signals that are to be received. The pass band of the filter is relatively narrow. It is therefore necessary that it can be tuned very accurately to the desired frequency. The desired frequency range may also change during use, for example to receive on a different radio frequency channel. In addition it is necessary that any frequency drift due, for example, to temperature variations altering the characteristics of the filter's components, can be corrected for or accommodated. Furthermore, it is likely that the actual values of the filter's components will vary from transceiver to transceiver within a nominal tolerance, and the filter must be capable of being adjusted to nevertheless achieve the desired response.

Figure 3 shows in more detail one simple example of a structure for the passive filter 40. The filter comprises an inductive limb 44 and a capacitative limb shown

generally at 45 connected in parallel between a node 50 (which runs from input node 42 to output node 43) and signal ground. The inductive limb 44 of the filter is provided by an inductance 46. The capacitative limb is provided by a discretely variable capacitative structure. The capacitative structure comprises one or more capacitors (in this case four capacitors 51-54) arranged in parallel between the node 50 and the signal ground. In series with each of the capacitors is a respective transistor switch 55-58 connected with its drain to the corresponding capacitor and its source to the signal ground. Each of the switching transistors is actuatable by a respective tuning signal 59-62 to its gate to change the impedance between its drain and source and thereby couple the respective capacitor to the signal ground. Thus the total capacitance of the capacitative limb can be digitally adjusted by means of the input lines 59-62. In this way the resonant response of the overall filter can be adjusted digitally. There may in addition be one or more capacitors fixedly coupled between node 50 and the signal ground. The capacitance of the capacitative limb is highest when all of the capacitances in the variable capacitative structure are switched in and lowest when all but the lowest switchable capacitor is switched out (or, in an embodiment in which a fixed path between node 50 and the signal ground is available, when all are switched out).

The filter behaves as a parallel LC filter, and its response is dependant on the total capacitance of the capacitative limb. The response of the filter can therefore be adjusted by means of the tuning signals on lines 59-62. A control unit 63 generates the tuning signals, and adjusts the response of the filter to the desired state. The individual tuning signals together represent a single overall tuning signal. The control unit 63 operates in response to a signal received at 64 which indicates whether an increase or decrease in the frequencies of the filter's response characteristics is needed. The signal 64 may be received from an overall control processor and/or from a feedback circuit.

It should be noted that the values of the capacitative and inductive components in the filter are selected so that they allow variation of the filter's response as sensitively as possible about the expected frequency range of the filter's pass

band. One particular way to achieve this is by careful selection of any fixed capacitative structure, and the provision of capacitors of sufficiently small values, or of a range of values.

In this embodiment digital signals are applied to the input lines 59-62, so that each switching transistor 55-58 is either fully on or fully off. Thus the transistor switching is essentially binary.

In a mass-produced transceiver it will be expected that there will be some variation in the real values of the inductive and capacitative components from one transceiver to the next, even if they have the same nominal values. If the transceiver is formed on an integrated circuit then typical variations are:  $\pm 30\%$  for resistors,  $\pm 10\%$  for capacitors and  $\pm 7\%$  for inductors; the values also being dependant on temperature. Therefore the same setting of the input lines 59-62 may not yield the same response in two nominally identical transceivers. The control apparatus 63, which can fix each input line 59-62 either high or low in order to achieve the desired response from filter 40, receives an input 64 and generates a digital output for each input line 59-61 accordingly. The operation of the control unit will be described in more detail below.

The filter 40 and control apparatus 63 can be implemented on a single integrated circuit. This approach offers a substantial size and cost reduction over prior designs using off-chip filter components. The control unit 63 allows the filter's response to be adjusted electronically rather than mechanically, saving time during manufacturing and allowing the possibility of the filter being adjusted on start-up of the transceiver or even during use in order to accommodate, for instance, temperature variations.

The transmit chain shown in figure 2 includes a mixer 70 which receives baseband signals at 71 and upconverts them to RF by mixing with an RF input from oscillator 72. The RF signals are then amplified by amplifier 73 before transmission.

Figure 4 shows in more detail one simple example of a structure for the oscillator 39. The oscillator circuit comprises transconductance stage 100 whose input 101 and output 102 are connected by inductor 103. The input 101 and output 102 are also each connected to signal ground by a respective capacitative structure 104, 105. Each capacitative structure comprises a series of capacitors 106-115 arranged in parallel between the input or output 101/102 and the low voltage. All but one of the capacitors of each structure is in series with a respective switching transistor by means of which the respective capacitor can be connected between the input/output and the signal ground in an analogous way to the operation of the capacitative section of the filter of figure 3. One of the capacitors of each structure (capacitors 110, 111) is fixedly connected between the input/output and the low voltage. Each capacitor has a counterpart of nominally equal capacitance in the opposite capacitative structure. Thus in the following pairs both capacitors have nominally equal values: 106/115, 107/114, 108/113, 109/112, 110/111. The transistors corresponding to the paired non-fixed capacitors are actuatable by a common tuning signal on lines 119-122. When the oscillator is formed on a single IC it will be expected that these values are exactly equal.

The oscillation frequency of the oscillator is discretely variable by means of the tuning signals on lines 119-122. In this embodiment digital signals are applied to the input lines 119-122, so that each switching transistor is either fully on or fully off.

The oscillator 39 could be controlled by its own control apparatus. However, it is preferred that it is controlled by the same control apparatus 63 as is used to control the filter 40. It is most preferred that the capacitor arrays 51-54, 106-109 and 115-112 are substantially the same or are related in value in a known way. Then similar signals can be used to control the respective switching transistors via switching lines 59-62 and 119-122 to achieve a common effect on the operating frequencies of filter 40 and oscillator 72 as will be described in more detail below.

In one preferred embodiment the filter 40 and the oscillator 39 are formed on the same integrated circuit, with each of the capacitors of their respective capacitor arrays being nominally the same as the corresponding capacitor in the other arrays. For example, the corresponding capacitors may be formed on the integrated circuit with the same dimensions and materials and under the same manufacturing process. Then, although the actual value of each capacitor might vary significantly from its nominal value due to inaccuracies in the process of manufacturing the integrated circuit, it is likely that the actual values of the corresponding capacitors will be very similar because both have been subject to the same manufacturing parameters. Therefore, the actual values of the corresponding capacitors in the arrays are likely to be very similar.

In many situations there will be a known relationship that is desired between the operating frequency of the filter and the oscillator - for example the frequency of the oscillator may be desired to be the same as the centre pass frequency of the filter, or one may be desired to be a known multiple (e.g. an integer multiple) of the other. When the variable capacitative components of the filter and the oscillator can be assumed to have essentially the same values, the circuits can be arranged so that the same control signals can be used to control each of those components and thereby adjust the operating frequencies of both the filter 40 and the oscillator 72 whilst maintaining a known relationship between the two. That relationship is dependant on the values of the fixed inductive and capacitative components in the filter and oscillator circuits. Therefore, in that embodiment the lines 59-62 and 119-122 may be connected respectively together, so that lines 59 and 119 give the same signal, lines 60 and 120 give the same signal; lines 61 and 121 give the same signal and lines 62 and 122 give the same signal. When the signals are scaled relative to each other this may be achieved by digital processing (e.g. simple shifting of the bit significance) of individual tuning signals.

The filter and the oscillator may be arranged so that they are not intended to receive common tuning input signals in the manner described above. However, it is preferred that they are designed to receive tuning signals that are of a fixed

relationship to each other, for example by one tuning signal being derived from the other.

In other embodiments the values of the capacitative components could be different. Preferably, each capacitor of the filter is scaled commonly to the respective capacitor of the oscillator, most preferably with each capacitor of the filter having a value that is an integer multiple of the value of its counterparts in the oscillator, or the capacitors in the oscillator having a value that is an integer multiple of their counterpart in the filter. When the multiple is an integer multiple, for example 2, 3, 4, or 5 it is easier to ensure common scaling of the capacitors, for instance by forming a larger capacitance from a number of smaller capacitors.

A potential advantage of all these arrangements is that there is no need to control the filter and the oscillator separately. Because the behaviour of the filter and the oscillator is linked in a known way by the relationship between their designs and components a single tuning arrangement can be used.

In order for the tuning signals to the filter and the oscillator to be conveniently related a number of measures may be adopted. The overall design of the filter and the oscillator, and the values of the fixed and variable components of the filter and the oscillator are preferably chosen so that the available adjustment of the circuits of the filter and oscillator produce the desired related alteration of their characteristics. Also, it is preferred that operational frequency of the oscillator is an integer multiple of a frequency that lies in or near the pass band of the filter.

A number of ways are available to derive the control signals for trimming the filter 40 and the oscillator 39 to control their operating frequencies. The trimming could be done at the manufacture stage, each time the transceiver is switched on or continuously or periodically during use. Since each trimming capacitance is switched fully into or out of the circuit the trimming is a digital operation. The setting of the switching transistors can be represented as a binary number with one digit corresponding to the switching input to each switching transistor. The

setting of the trimming capacitors can therefore be stored digitally by memory of the radio terminal during manufacture or use and reproduced exactly when required by recalling the appropriate setting from memory. More than one setting could be stored (either at manufacture or during use) for different applications, such as different operating frequencies or temperatures.

Figure 5 shows one preferred arrangement for controlling the oscillator 39 and, by means of common inputs to its switching transistors, the filter 40. The output of the oscillator 39 passes to a PLL 124 comprising a divider 126, a phase detector 125 and a loop filter 127. These could feed back to an input of the oscillator and/or to the oscillator control unit 63. The switching inputs 119-121 etc. of the oscillator are driven from an oscillator control unit 63. The control unit is under the supervision of a main processor 128 of the radio terminal. The control unit 63 monitors the voltage at input 108. The control unit adjusts the switching of the trimming capacitors with the aim that the voltage at 108 is as close as possible to a set value.

In the embodiments shown in figures 3 to 5, the filter and oscillator are discretely variable by means of digital tuning signals. The use of digital tuning signals is preferred because digital control signals are easily generated by digital control circuitry, discrete circuits such as the capacitor arrays of figures 3 and 4 are readily arranged to have similar and/or nominally identical values, and because a set of digital control signals can readily be stored in a memory of the control processor 63 for recall and re-use when required. However, it is possible for common analogue tuning signals to be used. For example, figure 6 illustrates one example of a filter circuit analogous to that of figure 3 in which the capacitative limb includes a varicap diode 140 that can be adjusted by an analogue signal on line 141 to alter the response of the filter. A similar arrangement in can be used for the variable capacitance(s) of a corresponding oscillator.

Each of the arrays of capacitors may include any number of capacitors from two upwards, or from one upwards if there is a fixed capacitor in parallel too. The

values of the trimming capacitors in a single array could be the same or different. If the values of the trimming capacitors are different then the same number of trimming capacitors can be used to allow trimming over a wider range. The trimming capacitors could suitably provide 128 equal steps of total capacitance. This could, for instance, be achieved by 128 capacitors of the same value, or 7 capacitors of values scaled as  $2^n$ .

The bandwidth of the filter may be sufficient that the filter does not need to be adjusted as precisely as the oscillator. In that case in the tuning circuit for the filter the equivalent capacitors to the smallest (least significant) capacitors of the tuning circuit for the oscillator could be omitted.

The radio terminal could be a radio transmitter and/or receiver. The terminal could operate according to any suitable scheme, including TDMA (time division multiple access) and FHSS (frequency hopped spread spectrum). The oscillator could be used in other applications such as tone generation.

The applicant draws attention to the fact that the present invention may include any feature or combination of features disclosed herein either implicitly or explicitly or any generalisation thereof, without limitation to the scope of any of the present claims. In view of the foregoing description it will be evident to a person skilled in the art that various modifications may be made within the scope of the invention.

**CLAIMS**

1. A radio transmitter and/or receiver comprising:
  - an oscillator tuning circuit comprising an adjustable capacitor whose capacitance is adjustable by means of a first tuning signal;
  - a filter tuning circuit comprising an adjustable capacitor whose capacitance is adjustable by means of a second tuning signal;
  - an oscillator whose operational frequency is dependant on the reactance of the oscillator tuning circuit;
  - a filter for filtering signals in the course of transmission and/or reception, and whose response is dependant on the reactance of the filter tuning circuit; and
  - a tuning unit for generating the first and second tuning signals;wherein at least a part of the filter tuning circuit is a replica of at least a part of the oscillator tuning circuit and the tuning circuit is capable of generating one of the first and second tuning signals in dependence on the other of the tuning signals.
2. A radio transmitter and/or receiver as claimed in claim 1, wherein the said part of the oscillator tuning circuit comprises a plurality of selectively engageable reactive elements on which the reactance of the oscillator tuning circuit is dependant.
3. A radio transmitter and/or receiver as claimed in claim 2 or 3, wherein the said reactive elements are capacitors.
4. A radio transmitter and/or receiver as claimed in claim 2 or 3, wherein the said part of the filter tuning circuit comprises a plurality of reactive elements each corresponding to a reactive element of the oscillator tuning circuit.
5. A radio transmitter and/or receiver as claimed in claim 4, wherein the reactive elements of the said part of the filter tuning circuit are nominally of a common scale with respect to the corresponding elements of the said part of the oscillator tuning circuit.

6. A radio transmitter and/or receiver as claimed in claim 4 or 5, wherein the reactive elements of the said part of the filter tuning circuit are nominally identical to the corresponding elements of the said part of the oscillator tuning circuit.
7. A radio transmitter and/or receiver as claimed in any of claims 4 to 6, wherein the said reactive elements of the said part of the filter tuning circuit are nominally scaled as an integer multiple of the corresponding elements of the said part of the oscillator tuning circuit.
8. A radio transmitter and/or receiver as claimed in any of claims 4 to 7, wherein the said reactive elements of the said part of the oscillator tuning circuit are nominally scaled as an integer multiple of the corresponding components of the said part of the filter tuning circuit.
9. A radio transmitter and/or receiver as claimed in any of claims 2 to 8, wherein each of the said reactive elements of the oscillator tuning circuit is selectively engageable by a respective oscillator tuning switch means connected in series with it.
10. A radio transmitter and/or receiver as claimed in claim 9, wherein each of the said oscillator tuning switch means is responsive to a digital component of the second tuning signal.
11. A radio transmitter and/or receiver as claimed in any of claims 5 to 8 as dependant on claim 4, wherein each of the said reactive elements of the filter tuning circuit is selectively engageable by a respective filter tuning switch means connected in series with it.
12. A radio transmitter and/or receiver as claimed in claim 9, wherein each of the said filter tuning switch means is responsive to a digital component of the first tuning signal.

13. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the operational frequency of the oscillator is an integer multiple or fraction of a frequency that lies in or near the pass band of the filter.
14. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the filter tuning circuit and the oscillator tuning circuit are formed on a single integrated circuit.
15. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the first and second tuning signals each comprise a plurality of digital signals.
16. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the filter is part of a zero or near zero intermediate frequency radio receiver unit.
17. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the filter is a passive filter.
18. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the oscillator is a local oscillator for the transmitter and/or receiver.
19. A radio transmitter and/or receiver as claimed in any preceding claim, wherein the tuning signals are derived from monitoring the voltage in a phase locked loop which has the oscillator as one of its two inputs.
20. A radio transmitter and/or receiver unit substantially as herein described with reference to the accompanying drawings.

1 / 2

FIG.1

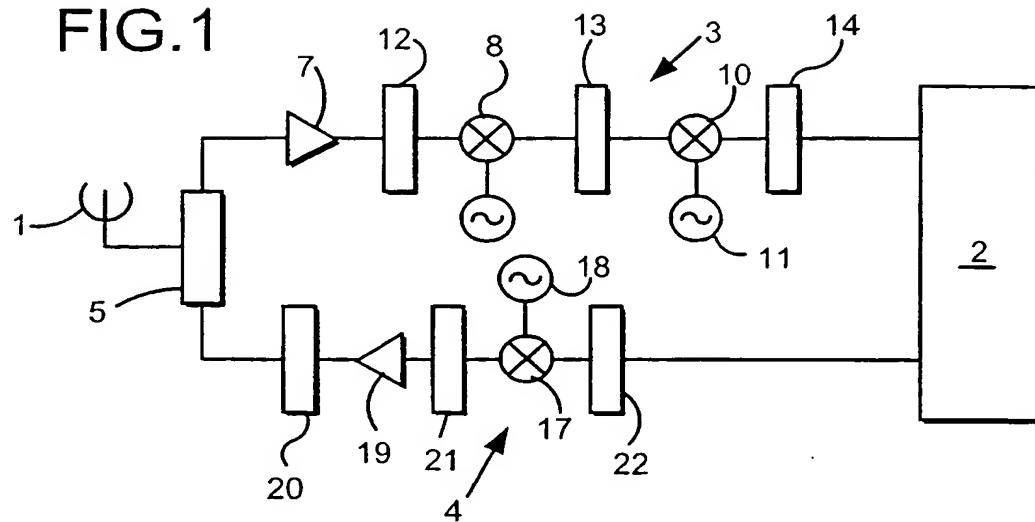


FIG.2

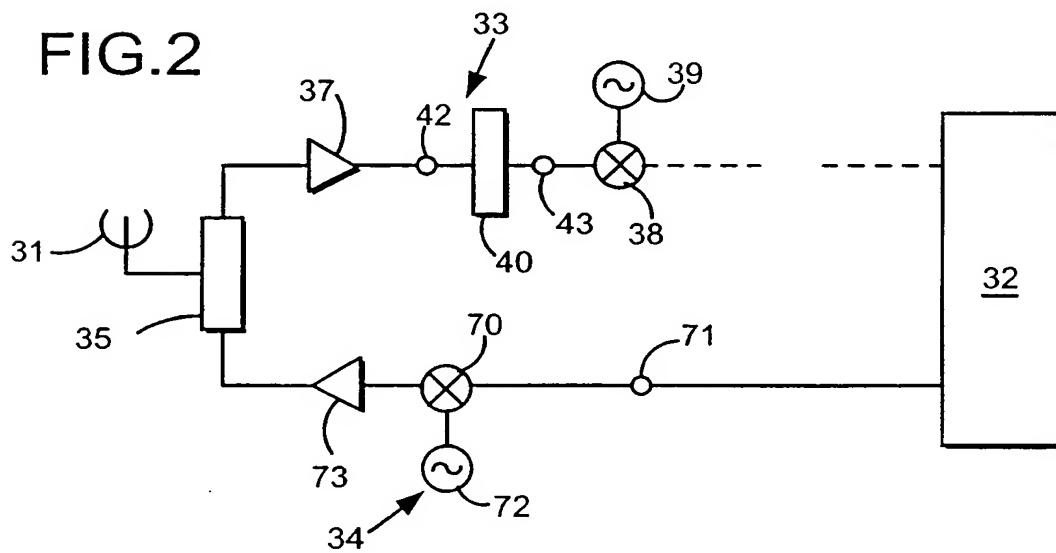
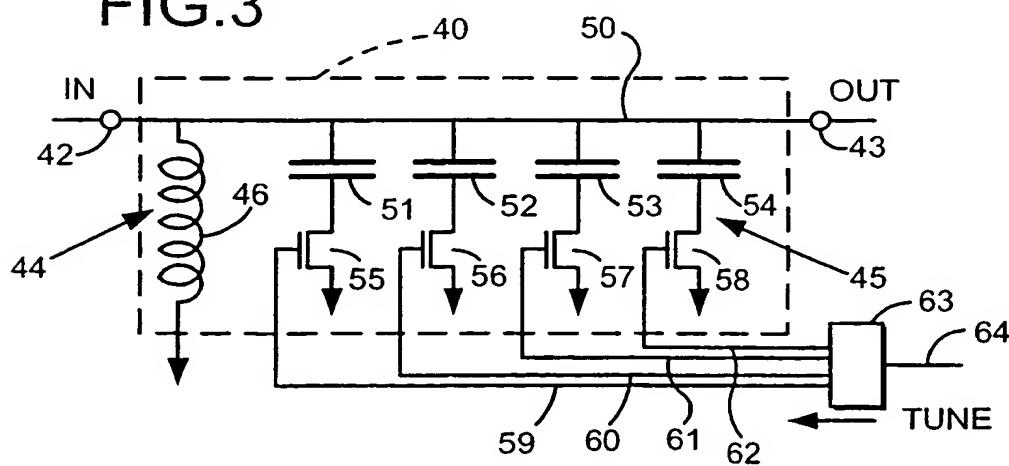
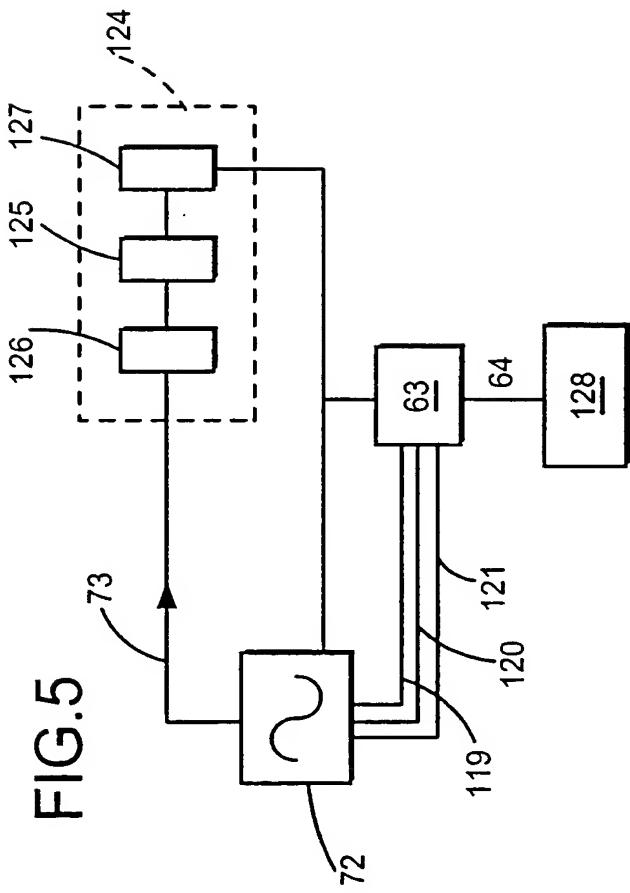
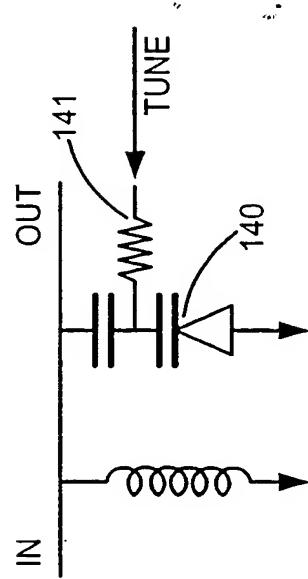
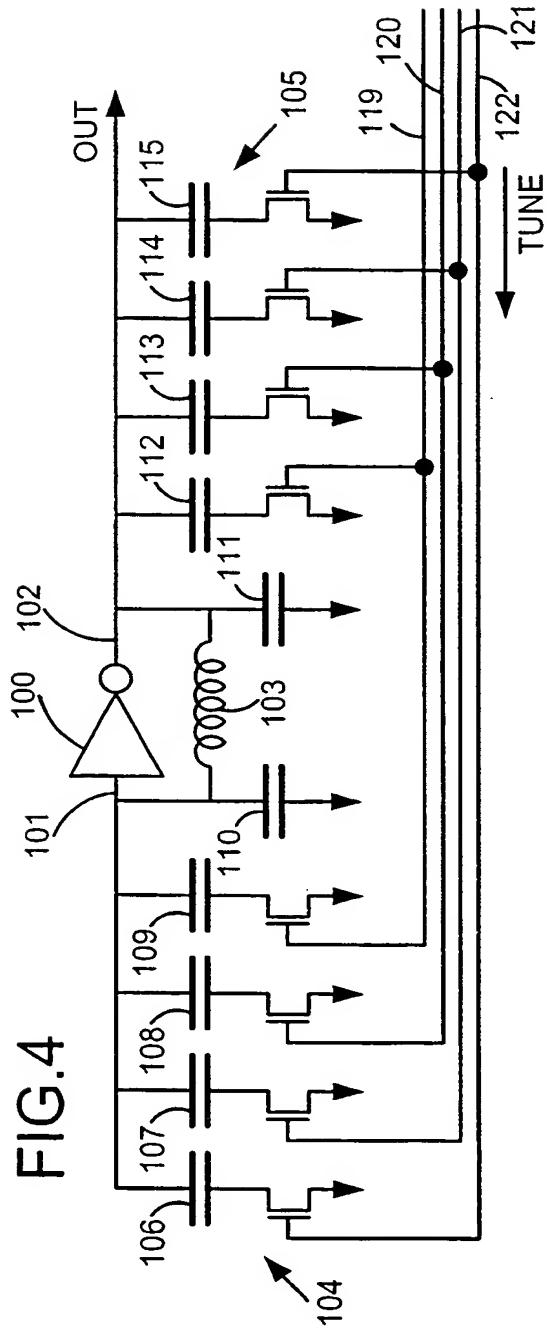


FIG.3



SUBSTITUTE SHEET (RULE 26)

2/2



# INTERNATIONAL SEARCH REPORT

International Application No  
PCT/GB 00/02759

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H03B5/12		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) IPC 7 H03B		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
P, Y	EP 0 951 147 A (KONINKL PHILIPS ELECTRONICS NV) 20 October 1999 (1999-10-20) the whole document ----	1-5, 8, 9, 19, 20
Y	EP 0 563 945 A (NIPPON ELECTRIC CO) 6 October 1993 (1993-10-06) column 6, line 1 - line 26; figure 6 ----	1-5, 8, 9, 19, 20
A	US 5 880 643 A (GAERDENFORS TORBJOERN ET AL) 9 March 1999 (1999-03-09) the whole document -----	1-20
<input type="checkbox"/> Further documents are listed in the continuation of box C.		<input checked="" type="checkbox"/> Patent family members are listed in annex.
<p>° Special categories of cited documents :</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"&amp;" document member of the same patent family</p>		
Date of the actual completion of the international search		Date of mailing of the international search report
6 November 2000		13/11/2000
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl. Fax: (+31-70) 340-3016		Authorized officer  Beasley-Suffolk, D

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No  
PCT/GB 00/02759

Patent document cited in search report	Publication date	Patent family member(s)		Publication date
EP 0951147	A 20-10-1999	CN 1235416	A 17-11-1999	
		JP 2000004180	A 07-01-2000	
EP 0563945	A 06-10-1993	JP 2985489	B 29-11-1999	
		JP 5284016	A 29-10-1993	
		US 5349309	A 20-09-1994	
US 5880643	A 09-03-1999	AU 1447899	A 07-06-1999	
		BR 9814969	A 03-10-2000	
		EP 1032970	A 06-09-2000	
		NO 20002264	A 18-05-2000	
		WO 9926335	A 27-05-1999	